



Features

- 32 General Purpose Isolated FET Switches
- 1 Amp Maximum Switched Current
- ± 60 VDC or 250 VAC Maximum Switching Voltage
- 512 state scan list
- PXI triggers
- Programmable scan advance delay
- Drivers Provided for: Windows 2000/XP/NT/ME/9x
- Programming: Visual Basic, Visual C/C++
LabView, LabWindows/CVI, CVI Function Panels



Function

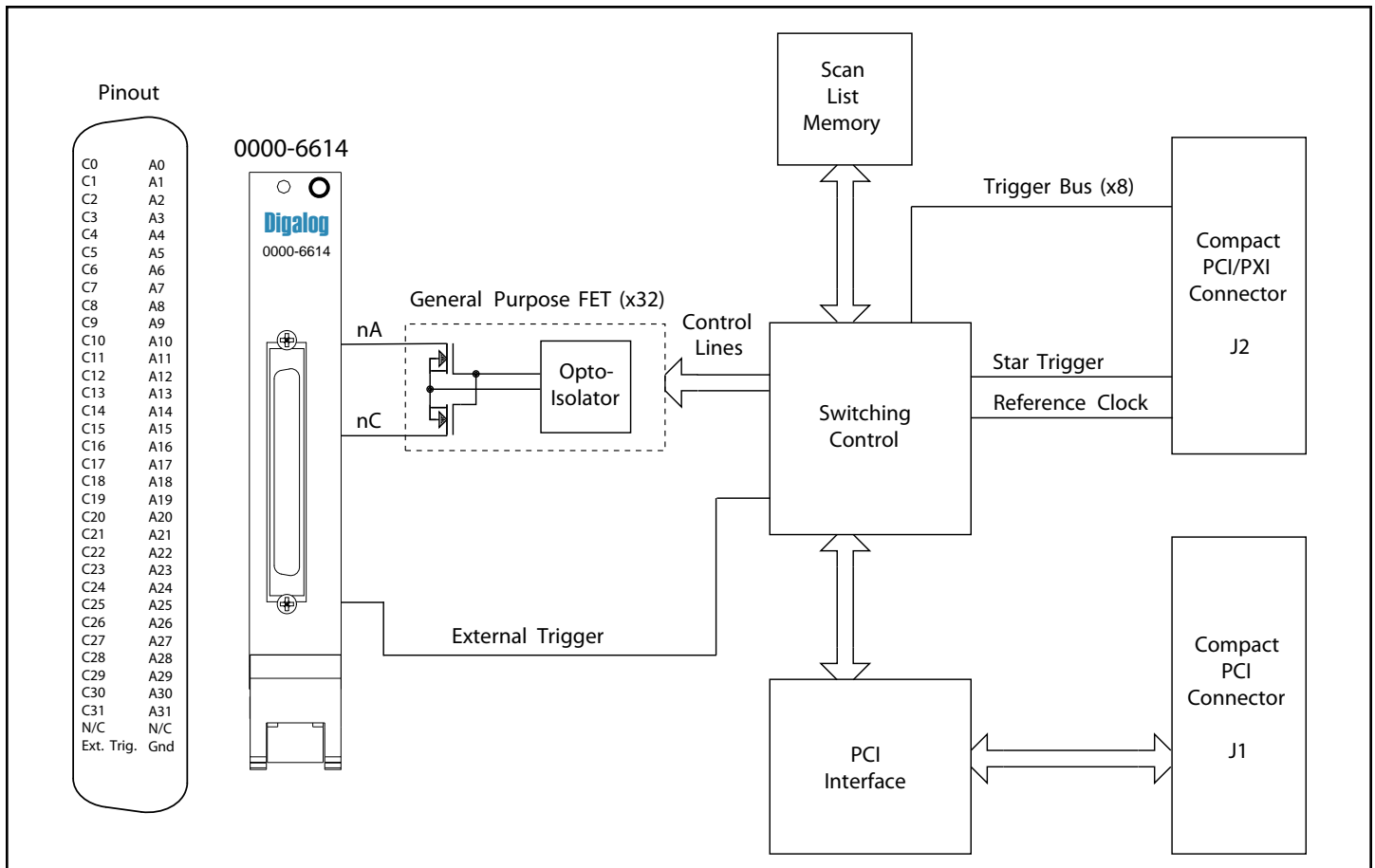
Digalog Switch Cards are standard PXI switching cards that use Isolated FET switches as an alternative to mechanical relays. They are utilized to benefit from the power switching capabilities of FET based switches.

This 32 channel FET switch card consists of thirty-two (32) FET switches, each capable of switching 1 Amp of load current at ± 60 V of load voltage. These FET switches are controlled with simple functions to the card, or they may be controlled with a pre-loaded list of switch states (512 states). Several trigger sources may be used to advance this state list.

Every time the card's switches change states (either in manual mode or the scan advance mode), an automatic switch operation delay occurs. After that completes, a pre-programmed scan advance delay occurs. In manual mode, the software may be configured to wait before setting the next state. After the completion of these delays, an output trigger may be sent to other cards on the PXI bus.

PXI Standard

The 6614 32 Channel FET Switch Card follows the PXI Specification for peripheral cards. This standard, controlled by the PXI Systems Alliance, builds upon the CompactPCI Specification. The PXI specification adds several enhancements to CompactPCI, most notably a trigger bus, a local bus, a Star trigger, and a reference clock. The specification also includes more stringent system mechanical and software requirements.



PXI Details	
PXI Specification	Revision 2.0 (July 28, 2000)
Form Factor	PXI 3U (half-height), single slot (0.8")
PCI Interface	
Bus Width	32-Bit
I/O Voltage	5V
Bus Speed	0-33MHz
Bus Master	No
Interrupts	No
Hot Swap	No
PXI Signals	
PXI Trigger Bus	Source and Destination
PXI Star Trigger	Destination
PXI Local Bus	No
PXI Reference Clock	10MHz Required
Required Power	
+5V	All Switches Open, 0.06A Max. All Switches Closed, 0.41A Max.
Generated Wattage	
	All Switches Open, 0.3W Max. All Switches Closed at 0.5A, 6.1W Max. All Switches Closed at 1.0A, 18W Max.

Switching Details	
Number and Type	32 Form A
Output Characteristics @ 25°C	
Maximum Resistance (1A pulsed, 100ms)	0.5Ω Max.
Maximum Load Voltage (peak)	+60V
Maximum Load Current	1A
Maximum Operating Time	2.5ms Max.
Maximum Off-State Leakage @ 60V	1uA
Maximum Capacitance	500pF

Connector Details		
Cable Connectors	Fujitsu	FCN-237R068-G/F
	Honda	PCS-XE68MC1G1

System Operating Environment	
Operating Temperature	0 – 35° C, 32 – 95° F
Humidity	20% to 80% Relative Humidity

External Trigger Input	
Trigger	Rising edge, TTL levels
Pulse width	30ns minimum
Time between triggers	2.5ms (Default)

Programming Details	
Scan List Depth	512 States
Scan Delay (Default)	2.5ms

Specifications are subject to change without notice.

Z-2559 PXI 6614 (Rev. 021609)